

10/045697
1/14/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPLICANT 10045697	FILING DATE 01/14/2002	CLASS 369	SUBCLASS 047310	GAU 235T	EXAMINER Battaglia
**APPLICANTS: Fujiwara Tsuneo; 2652					
**CONTINUING DATA VERIFIED:					
<h1>BEST AVAILABLE COPY</h1>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-9524 01/17/2001					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO.	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		56853 (70904)	
Verified and Acknowledged Examiners's initials					
TITLE : Phase correction circuit and disk reproduction device using the same					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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